

CLEAN VERSION OF AMENDMENTS

IN THE CLAIMS

Please amend claims 7 and 19, to read as follows:

1 7. (Twice Amended) An apparatus, comprising:
2 an inputting device inputting a display data channel of a monitor into a computer;
3 a driving device driving the inputting device with a predetermined electric signal;
4 an interfacing section indicating whether the display data channel of the monitor is inputted
5 into the computer and outputting the same voltage signal as an initial signal, the outputted voltage
6 signal is switched at a different time according to a result of inputting the display data channel; and
D/ 7 a controller for controlling the driving device by generating the predetermined electric signal,
8 for analyzing the output signal from the interfacing section, and for determining whether or not the
9 result of inputting the display data channel is correct,
10 wherein after the display data channel is inputted into the computer and the interfacing
11 section outputs a high frequency signal, the controller determines that the display data channel is
12 normally inputted into the computer if the interfacing section outputs the same signal as the initial
13 signal at a first time, and after the interfacing section continues to output the high frequency signal
14 for a predetermined times after the first time, the controller determines that the display data channel
15 is abnormally inputted into the computer if the interfacing section outputs the same signal as the
16 initial signal at a second time.

1 19. (Amended) A method, comprising:

2 inputting a display data channel to a monitor by an inputting device;

3 driving said inputting device with a predetermined electric signal by a driving device;

4 indicating whether said display data channel of said monitor is inputted into said computer

5 and outputting a signal according to a result of said inputting by an interfacing section;

6 controlling said driving device by generating said predetermined electric signal;

7 analyzing said output signal from said interfacing section; and

8 determining whether said result of said inputting said display data channel is correct,

9 with said determining step determines that said display data channel is normally input into

10 said computer if said interfacing section outputs a same high frequency signal as originally input as

11 said predetermined electric signal at a first time; and

12 said determining step determines that said display data channel is abnormally input into said

13 computer after said interfacing section continues to output said high frequency signal at a second

14 time.